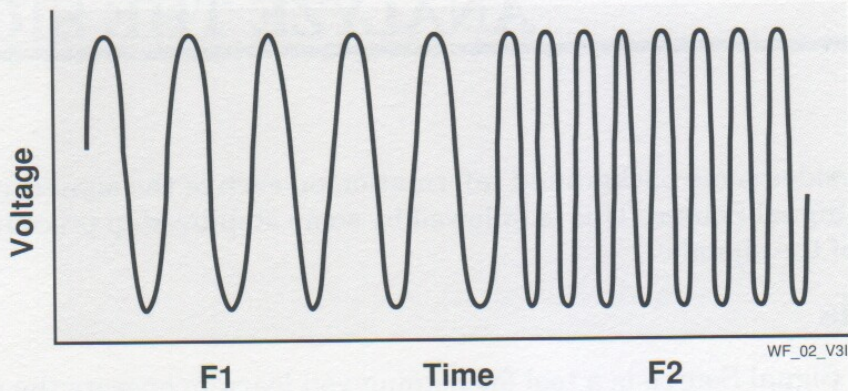
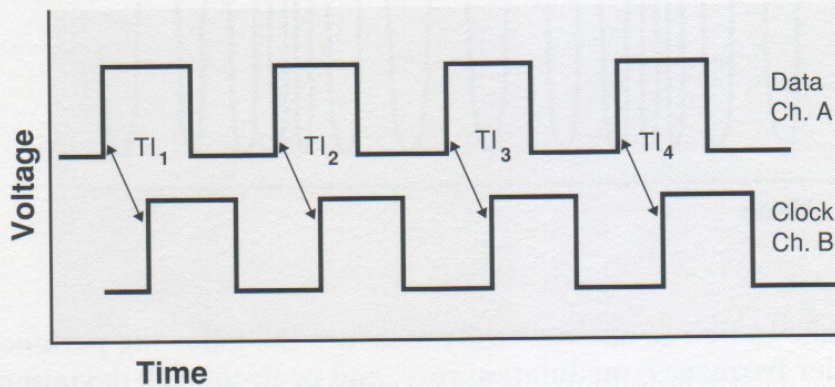


- VCO Step Response — A signal which jumps between two frequencies with some overshoot.



The VCO Step example shows how to adjust the Analyzer to see details on the VCO step, use markers to measure the frequency before and after the step, measure overshoot and transition time.

- Data-to-Clock Jitter — Two signals with a sinusoidal jitter component on the data signal with respect to the clock.



The Data-to-Clock Jitter example shows how to automatically measure the mean, rate, and the peak-to-peak of the data-to-clock jitter. Also, the histogram analysis capability shows the distribution of the time jitter.